

Embedded System Hardware Design Training - Advanced	
Day	Topic
Day1 (Morning)	<ul style="list-style-type: none"> • Understanding the SRS • Identification of class A components
Day1 (Afternoon)	<ul style="list-style-type: none"> • Architecting • Power estimation
Day2 (Morning)	<ul style="list-style-type: none"> • I/O estimation • Creating HDD from SRS
Day2 (Afternoon)	<ul style="list-style-type: none"> • Power supply conversion <ul style="list-style-type: none"> ○ Advanced topics in AC to DC ○ Advanced topics in DC to DC
Day3 (Morning)	<ul style="list-style-type: none"> • Digital I/O <ul style="list-style-type: none"> ○ High speed Interfaces
Day3 (Afternoon)	<ul style="list-style-type: none"> • Analog I/O <ul style="list-style-type: none"> ○ Voltage I/O and Current loop ○ Signal conditioning
Day4 (Morning)	<ul style="list-style-type: none"> • Memory Interface <ul style="list-style-type: none"> ○ DDR2/3
Day4 (Afternoon)	<ul style="list-style-type: none"> • Serial communication Interface <ul style="list-style-type: none"> ○ CAN ○ USB ○ LAN
Day5 (Morning)	<ul style="list-style-type: none"> • Signal Integrity analysis
Day5 (Afternoon)	<ul style="list-style-type: none"> • Signal Integrity analysis
Day6 (Morning)	<ul style="list-style-type: none"> • Thermal Analysis
Day6 (Afternoon)	<ul style="list-style-type: none"> • Thermal Analysis
Day7 (Morning)	<ul style="list-style-type: none"> • EMI/EMC Compliance
Day7 (Afternoon)	<ul style="list-style-type: none"> • EMI/EMC Compliance